



PATENT  
ATTORNEY DOCKET NO.: 060301-5003-01

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: ) Confirmation No.: 3808  
Noraki HIRAGA )  
Application No.: 10/642,345 ) Group Art Unit: 2836  
Filed: August 18, 2003 )  
For: SEMICONDUCTOR INTEGRATED )  
CIRCUIT DEVICE WITH ENHANCED )  
RESISTANCE TO ELECTROSTATIC )  
BREAKDOWN )

Commissioner for Patents  
U.S. Patent and Trademark Office  
Alexandria, VA 22314

Sir:

**RESPONSE TO ELECTION/RESTRICTION REQUIREMENT**

In response to the restriction requirement set forth in the Office Action mailed January 10, 2005, the period for response to which extends through February 10, 2005, Applicant elects the invention defined by the Examiner as Group I, claims 20-28, 31, and 36, for examination on the merits.

Except for issue fees payable under 37 C.F.R. §1.18, the Commissioner is hereby authorized by this paper to charge any additional fees during the entire pendency of this application including fees due under 37 C.F.R. §1.16 and 1.17 which may be required, including any required extension of time fees, or credit any overpayment to Deposit Account 50-0310.

Respectfully submitted,

**MORGAN, LEWIS & BOCKIUS LLP**

Dated: February 9, 2005

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